

FIG. 1

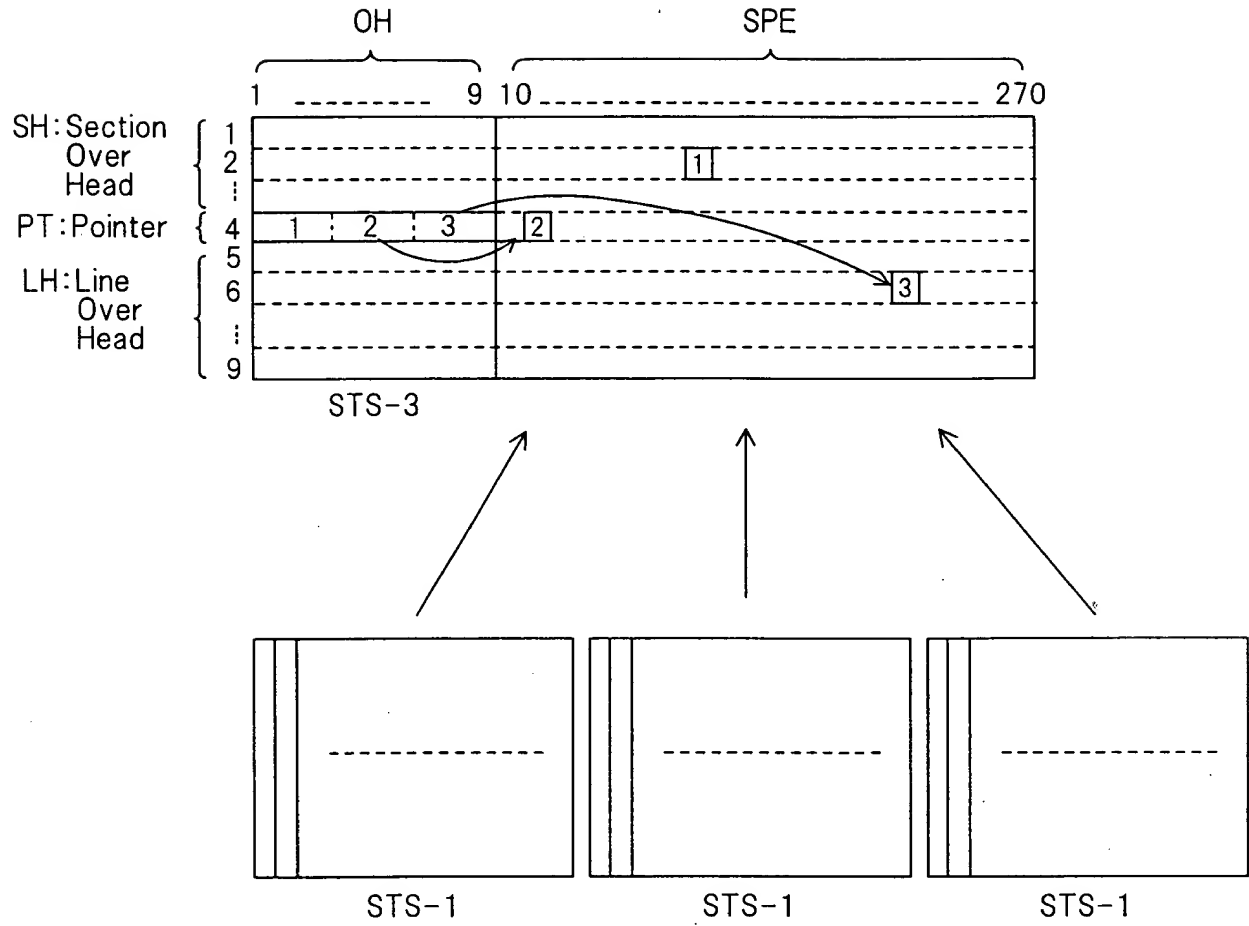


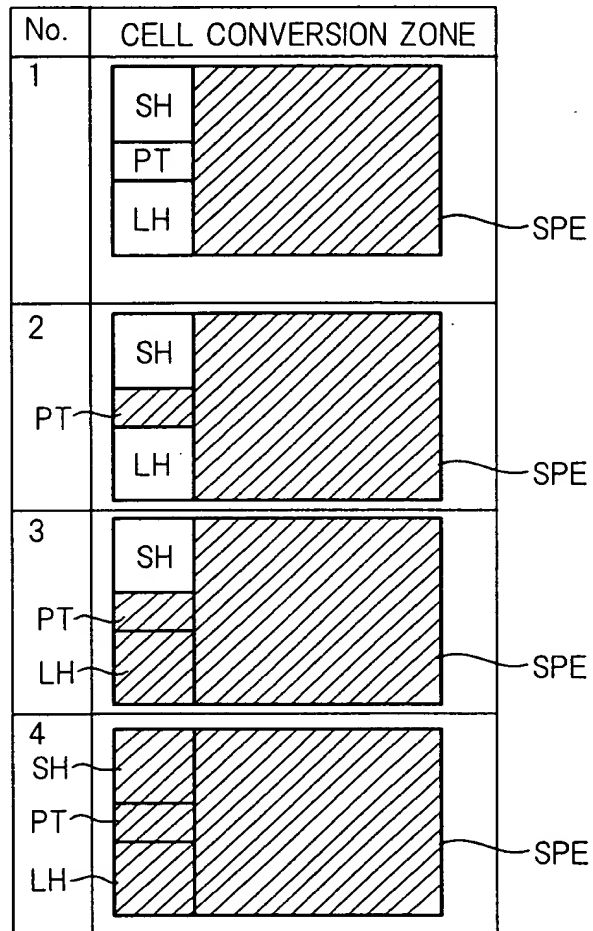
FIG. 2

FIG. 3

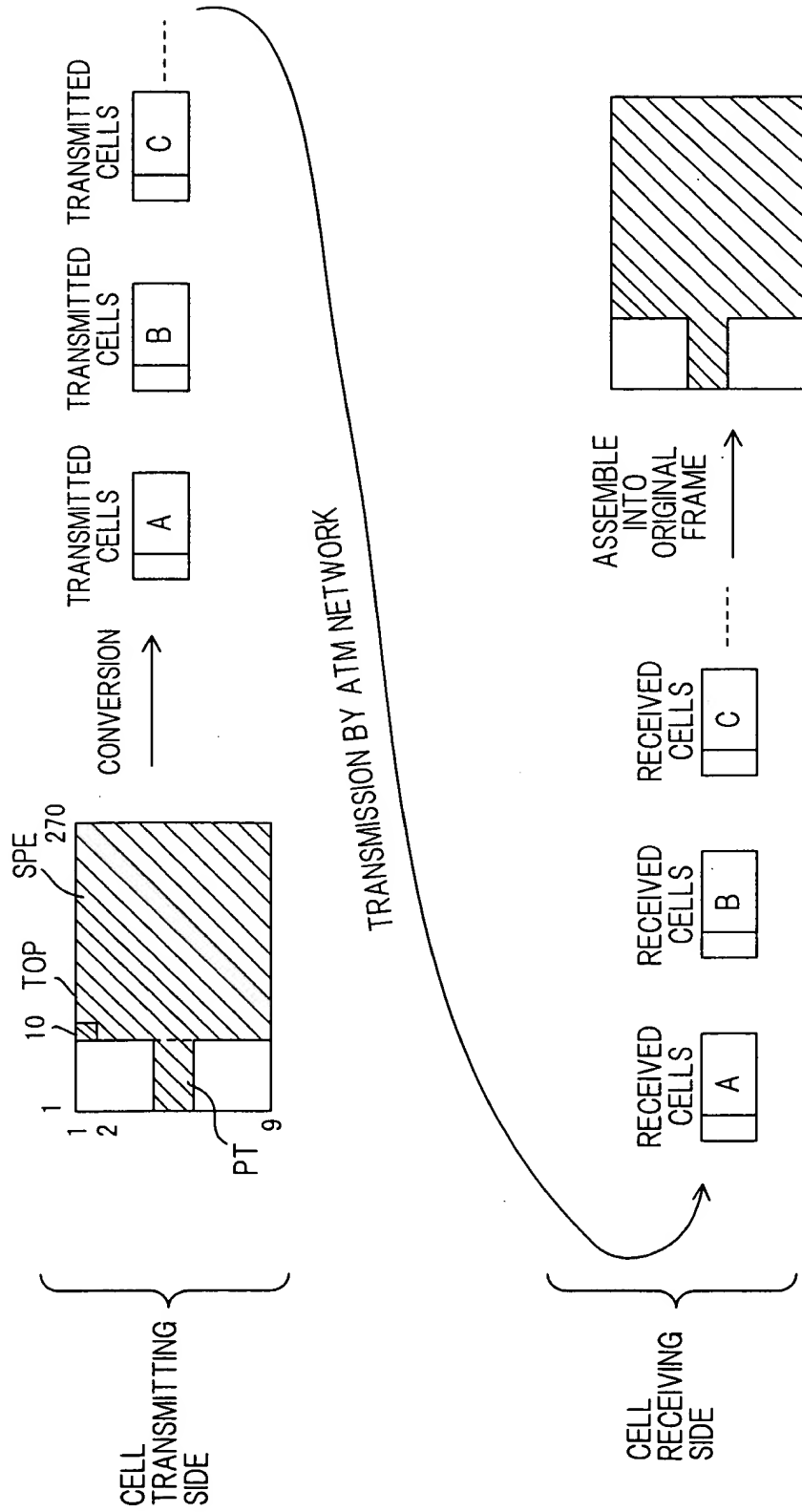
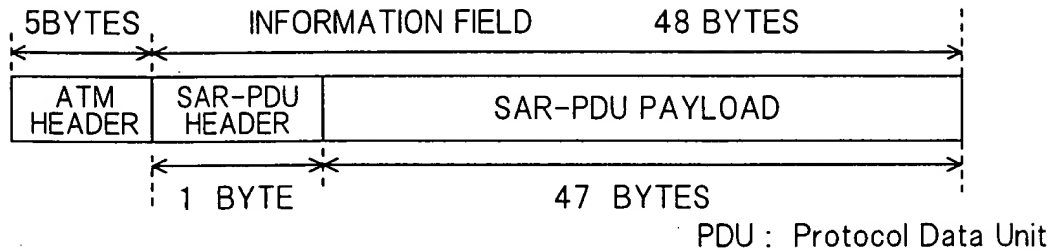
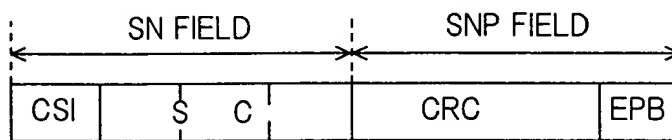


FIG. 4**FIG. 5**

CSI : Convergence Sublayer Identifier
 SC : Sequence Count
 CRC : Cyclic Redundancy Check
 EPB : Even Parity bit

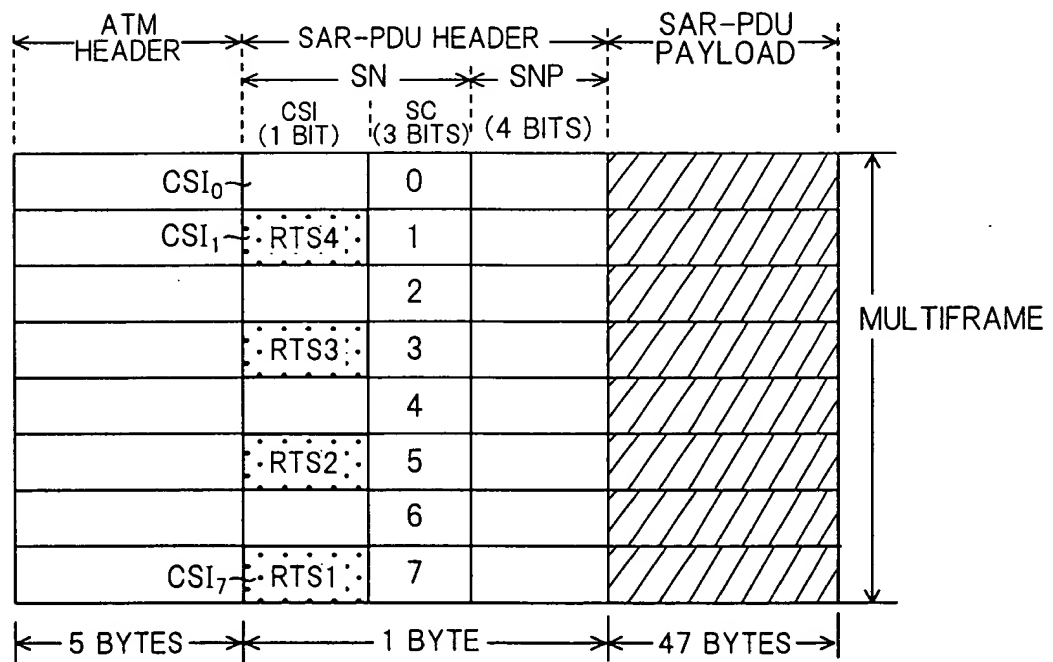
FIG. 6

FIG. 7A

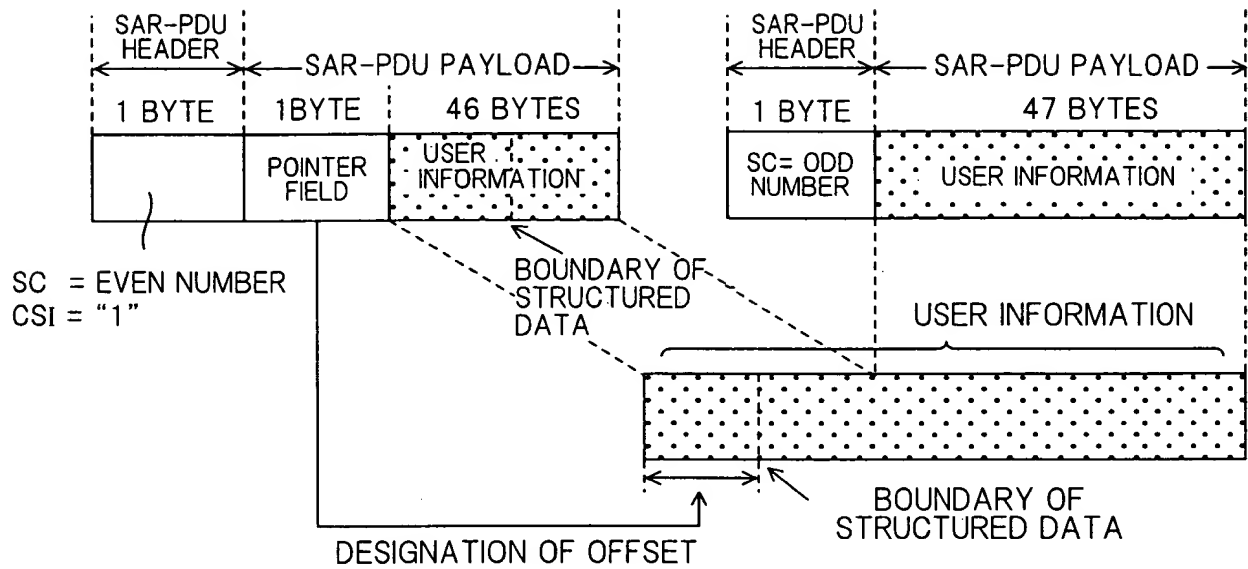


FIG. 7B

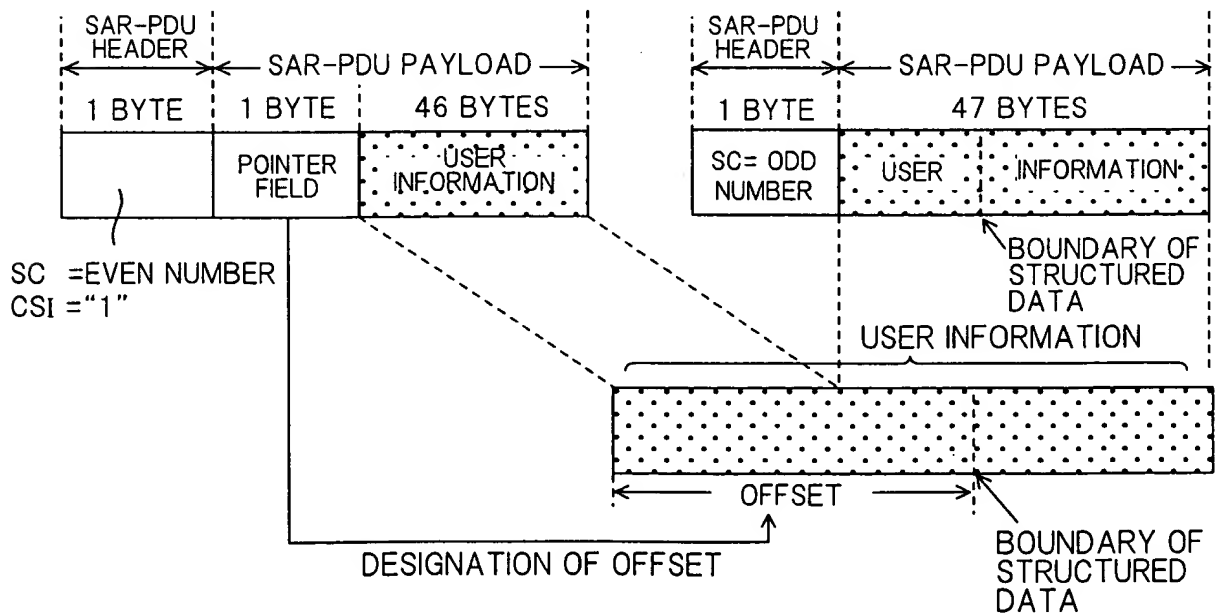


FIG. 8

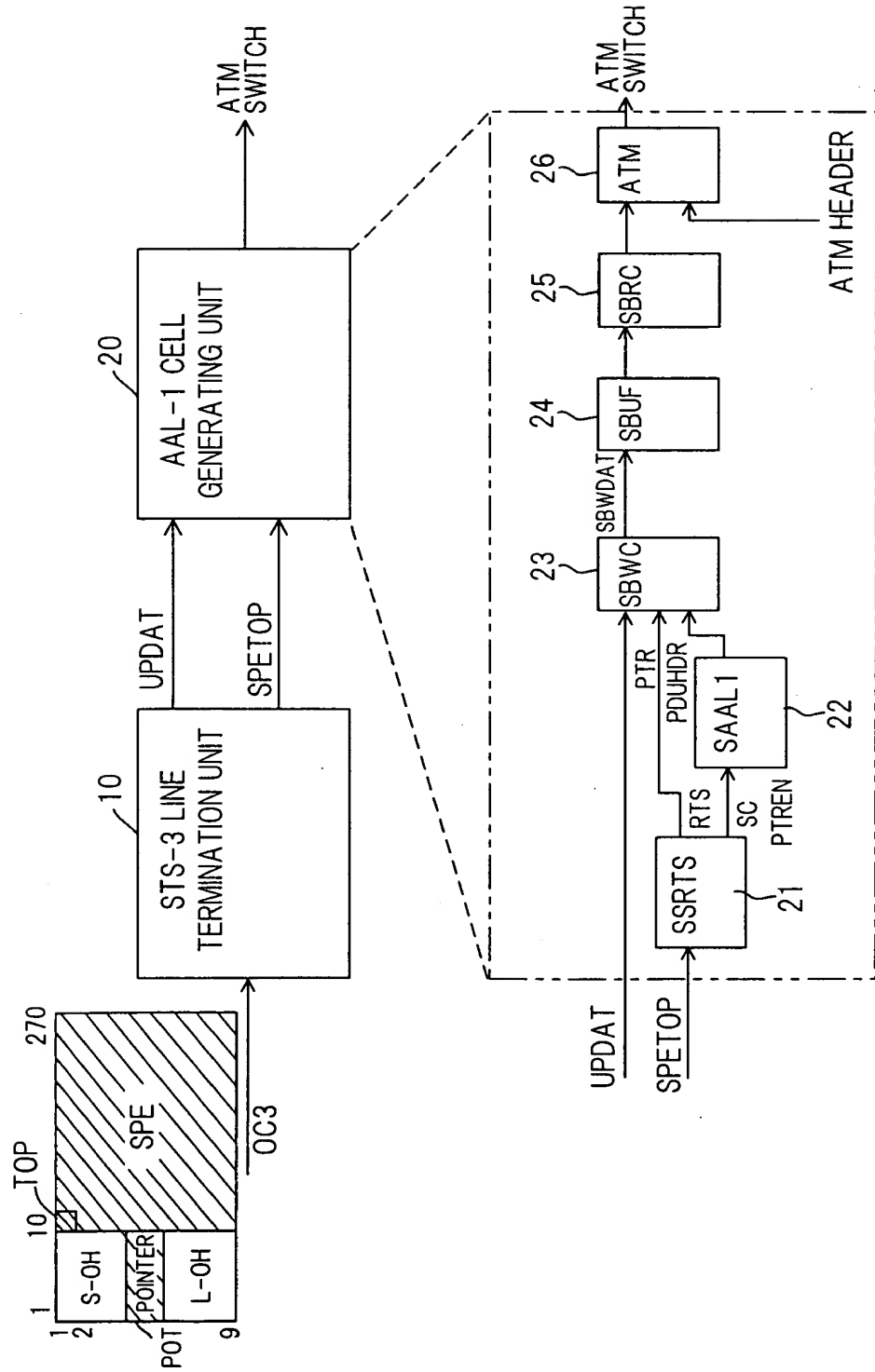


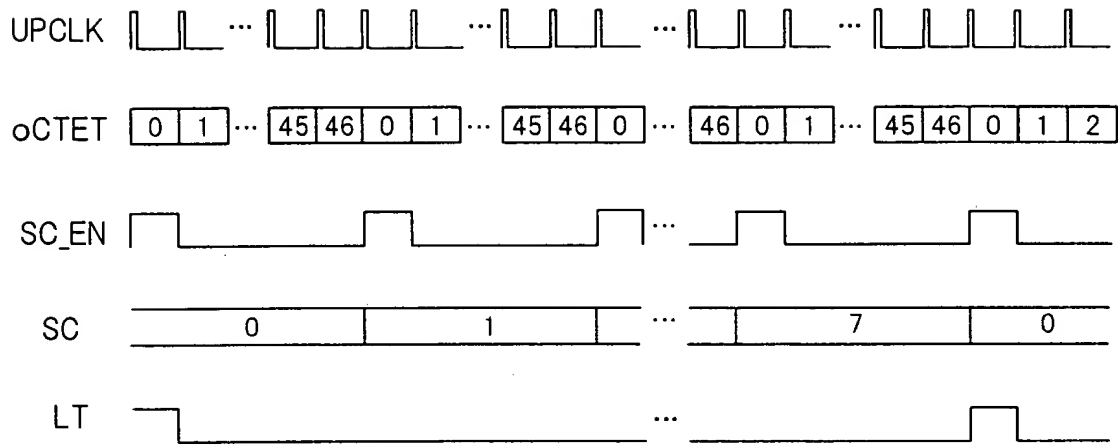
FIG. 10

FIG. 10

FIG. 11

POSITION REPORTED BY TOP



A1#1 0	A1#2 1	A1#3 2	A2#1 3	A2#2 4	A2#3 5	J0 6	Z0#2 7	Z0#3 8	SPE 9	SPE 10	SPE 11	-----	SPE 267	SPE 268	SPE 269
B1 270	X 271	X 272	E1 273	X 274	X 275	F1 276	X 277	X 278	SPE 279	SPE 280	SPE 281	-----	SPE 537	SPE 538	SPE 539
D1 540	X 541	X 542	D2 543	X 544	X 545	D3 546	X 547	X 548	SPE 549	SPE 550	SPE 551	-----	SPE 807	SPE 808	SPE 809
H1#1 810	H1#2 811	H1#3 812	H2#1 813	H2#2 814	H2#3 815	H3#1 816	H3#2 817	H3#3 818	SPE 819	SPE 820	SPE 821	-----	SPE 1077	SPE 1078	SPE 1079
B2#1 1080	B2#2 1081	B2#3 1082	K1 1083	X 1084	X 1085	K2 1086	X 1087	X 1088	SPE 1089	SPE 1090	SPE 1091	-----	SPE 1347	SPE 1348	SPE 1349
D4 1350	X 1351	X 1352	D5 1353	X 1354	X 1355	D6 1356	X 1357	X 1358	SPE 1359	SPE 1360	SPE 1361	-----	SPE 1617	SPE 1618	SPE 1619
D7 1620	X 1621	X 1622	D8 1623	X 1624	X 1625	D9 1626	X 1627	X 1628	SPE 1629	SPE 1630	SPE 1631	-----	SPE 1887	SPE 1888	SPE 1889
D10 1890	X 1891	X 1892	D11 1893	X 1894	X 1895	D12 1896	X 1897	X 1898	SPE 1899	SPE 1900	SPE 1901	-----	SPE 2157	SPE 2158	SPE 2159
S1 2160	Z1#2 2161	Z1#3 2162	M0 2163	Z2#2 2164	Z2#3 2165	E2 2166	X 2167	X 2168	SPE 2169	SPE 2170	SPE 2171	-----	SPE 2427	SPE 2428	SPE 2429

* LOWER VALUE IS VALUE OF STS-CTR COUNT

* BYTES WITHIN BOLD BORDER UNDERGO AAL-1 CONVERSION (RXoH = "0")

40093400

FIG. 12

POSITION REPORTED BY TOP

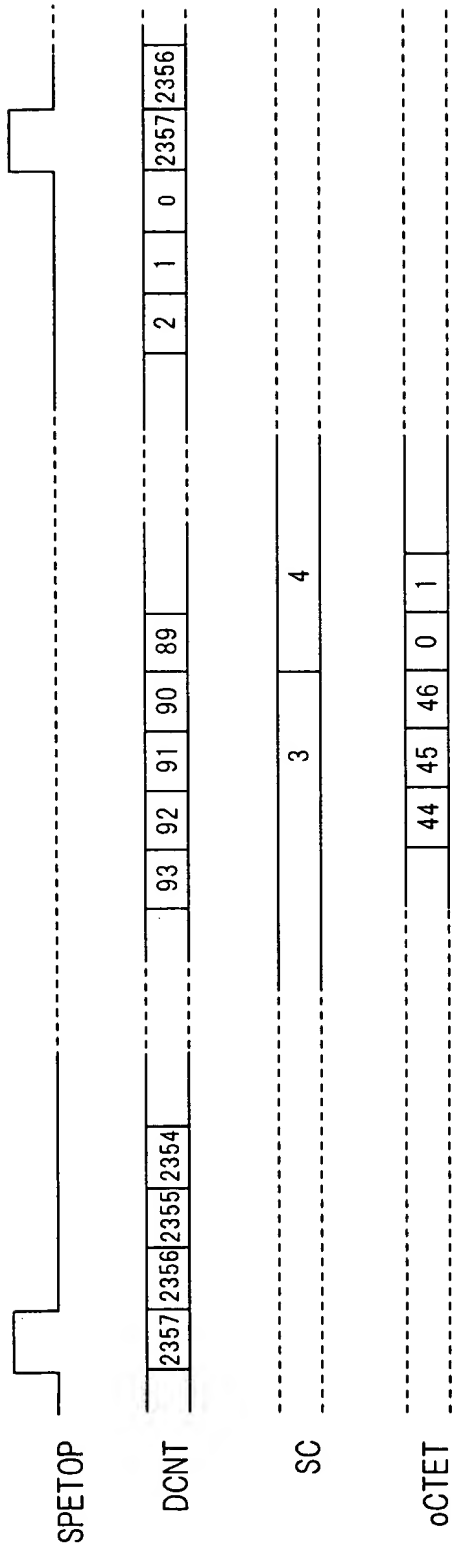


A1#1	A1#2	A1#3	A2#1	A2#2	A2#3	J0	Z0#2	Z0#3	SPE 2357	SPE 2356	SPE 2355	-----	SPE 2099	SPE 2098	SPE 2097
B1	X	X	E1	X	X	F1	X	X	SPE 2096	SPE 2095	SPE 2094	-----	SPE 1838	SPE 1837	SPE 1836
D1	X	X	D2	X	X	D3	X	X	SPE 1835	SPE 1834	SPE 1833	-----	SPE 1577	SPE 1576	SPE 1575
H1#1 1574	H1#2 1573	H1#3 1572	H2#1 1571	H2#2 1570	H2#3 1569	H3#1 1568	H3#2 1567	H3#3 1566	SPE 1565	SPE 1564	SPE 1563	-----	SPE 1307	SPE 1306	SPE 1305
B2#1	B2#2	B2#3	K1	X	X	K2	X	X	SPE 1304	SPE 1303	SPE 1302	-----	SPE 1046	SPE 1045	SPE 1044
D4	X	X	D5	X	X	D6	X	X	SPE 1043	SPE 1042	SPE 1041	-----	SPE 785	SPE 784	SPE 783
D7	X	X	D8	X	X	D9	X	X	SPE 782	SPE 781	SPE 780	-----	SPE 524	SPE 523	SPE 522
D10	X	X	D11	X	X	D12	X	X	SPE 521	SPE 520	SPE 519	-----	SPE 263	SPE 262	SPE 261
S1	Z1#2	Z1#3	M0	Z2#2	Z2#3	E2	X	X	SPE 260	SPE 259	SPE 258	-----	SPE 2	SPE 1	SPE 0

*LOWER VALUE IS VALUE OF DCNT COUNT

*BYTES WITHIN BOLD BORDER UNDERGO AAL-1 CONVERSION (RXoH = "0")

FIG. 13



BECAUSE SC = 4 (EVEN NUMBER) HOLDS ,
OCTET = 0 HOLDS AND THE VALUE OF DCNT
FALLS WITHIN RANGE OF 93 TO 1, THE VALUE
(89) OF DCNT AT THIS TIME IS ADOPTED AS
THE SDT POINTER VALUE

FIG. 14

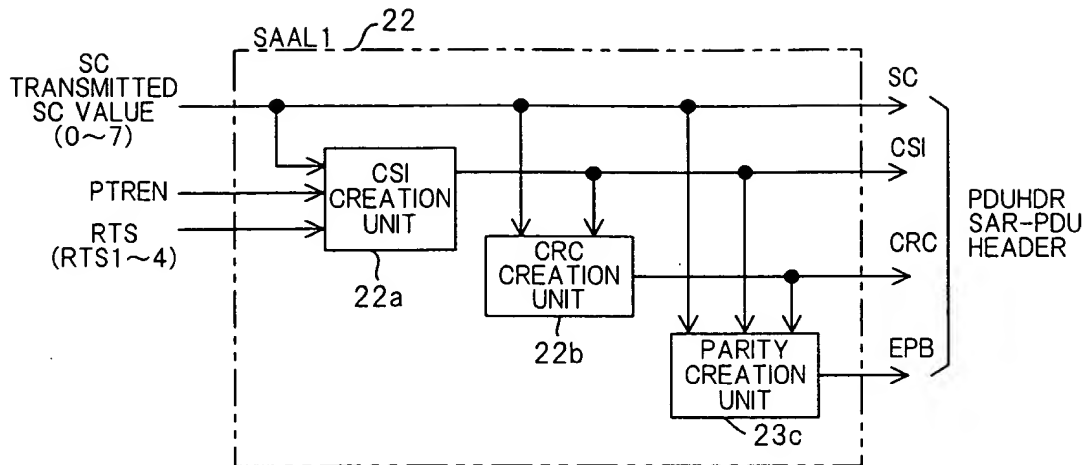


FIG. 15

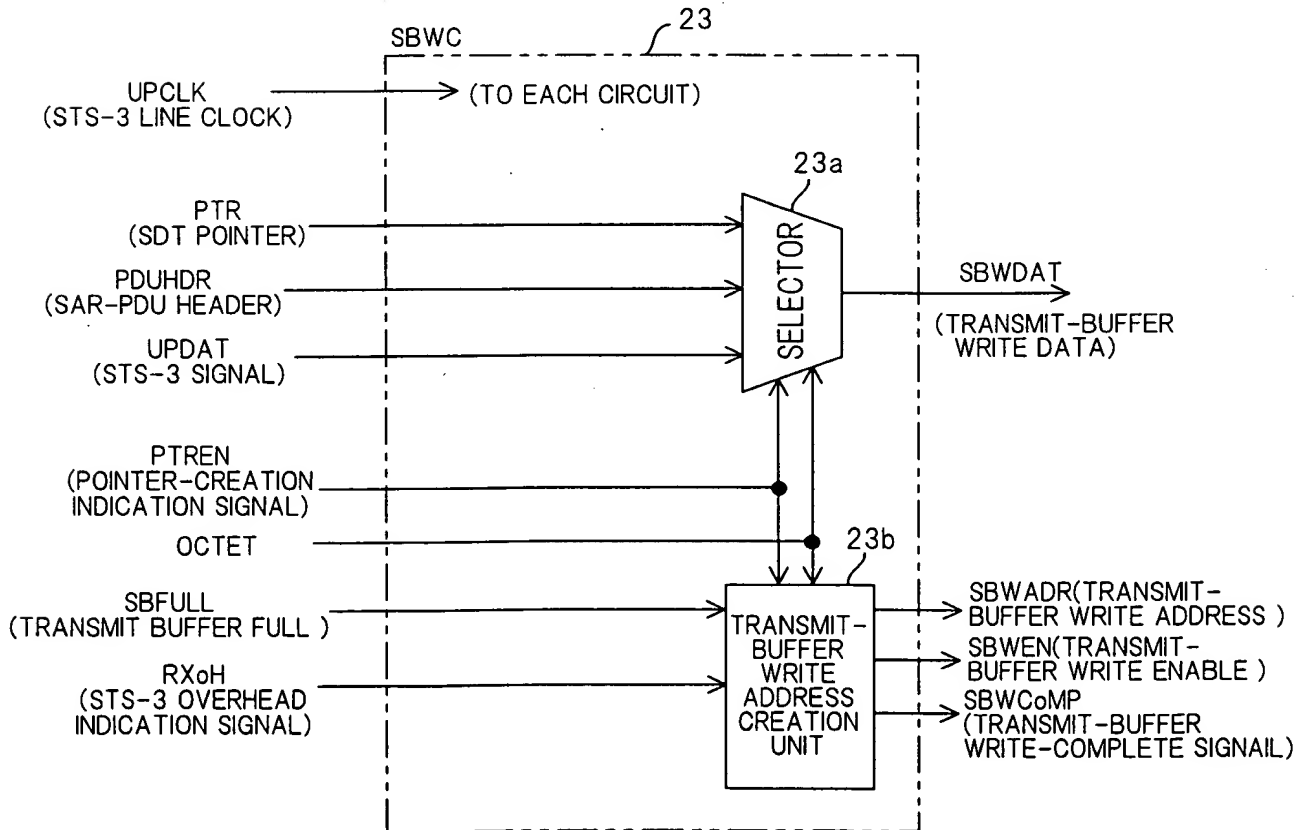
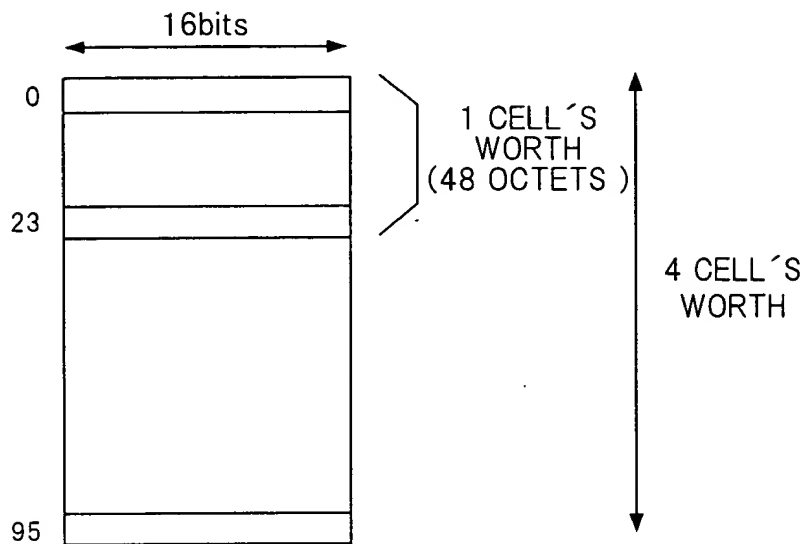
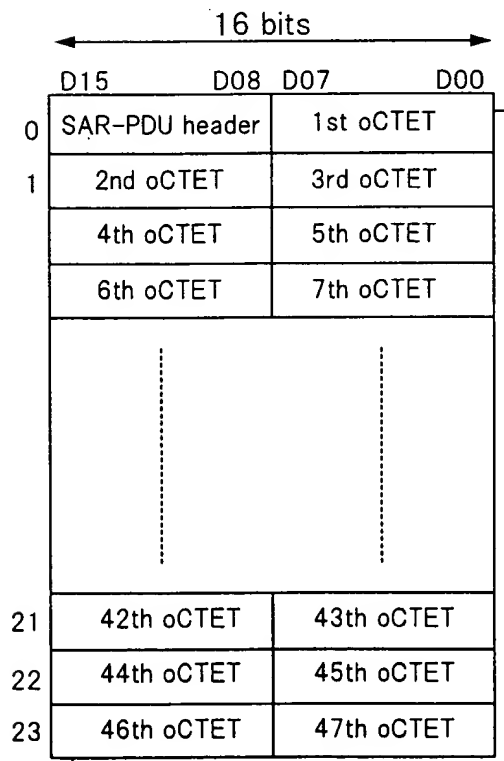


FIG. 16*FIG. 17A*

non P-format

*FIG. 17B*

P-format

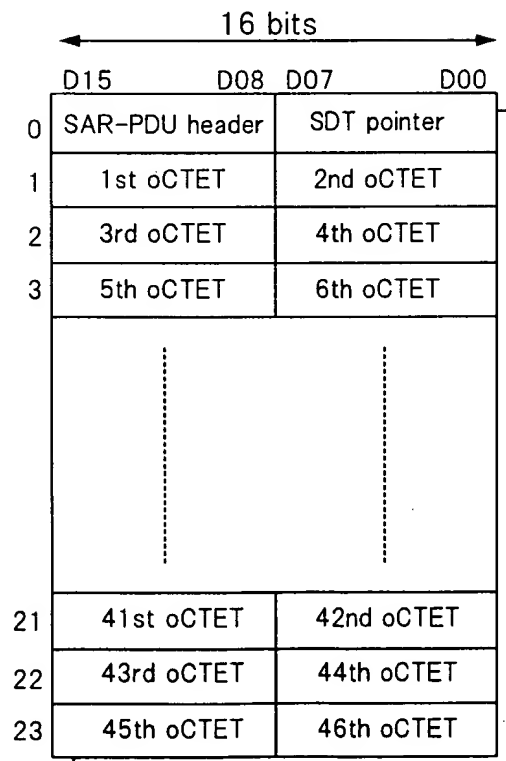


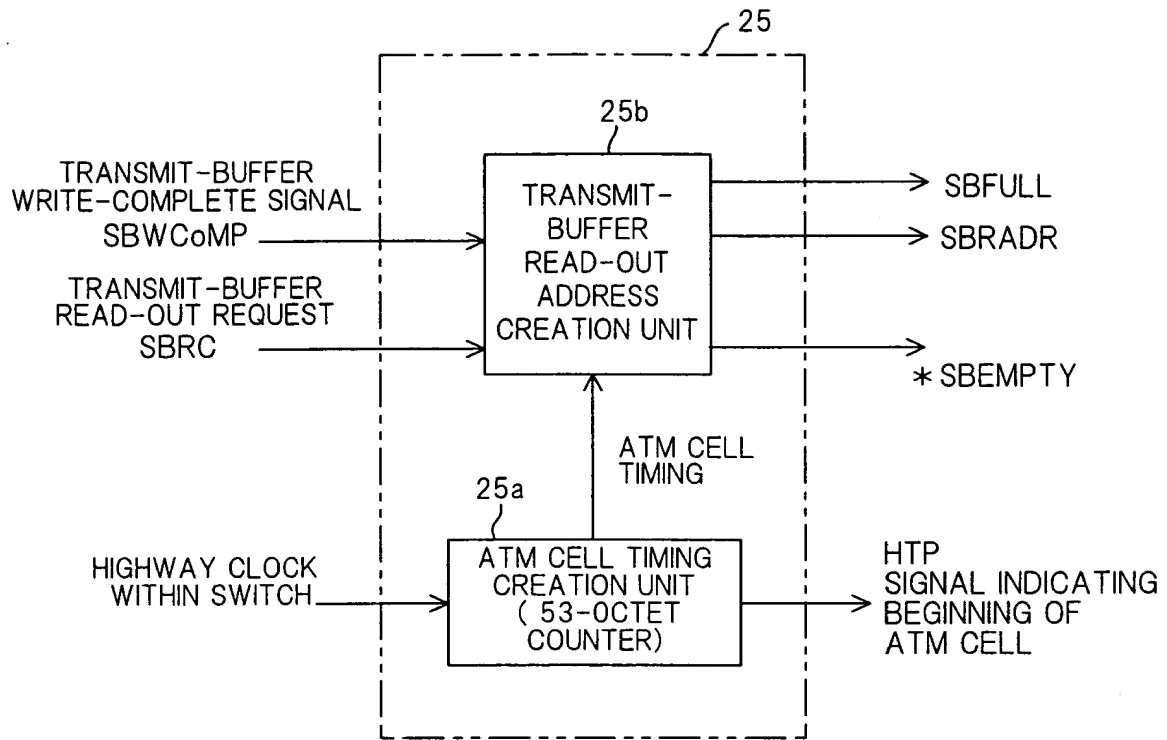
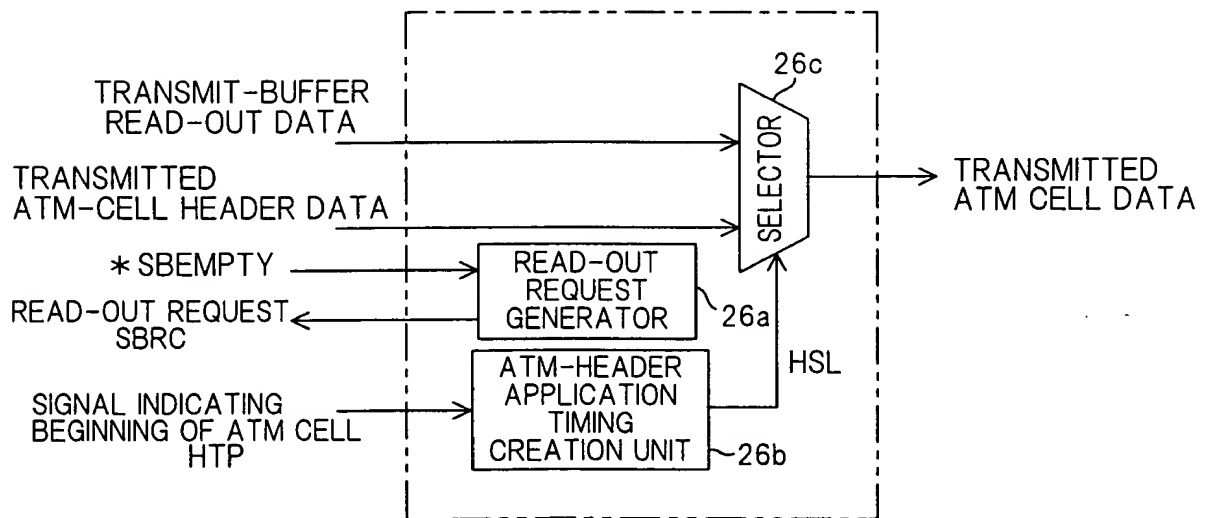
FIG. 18*FIG. 19*

FIG. 20

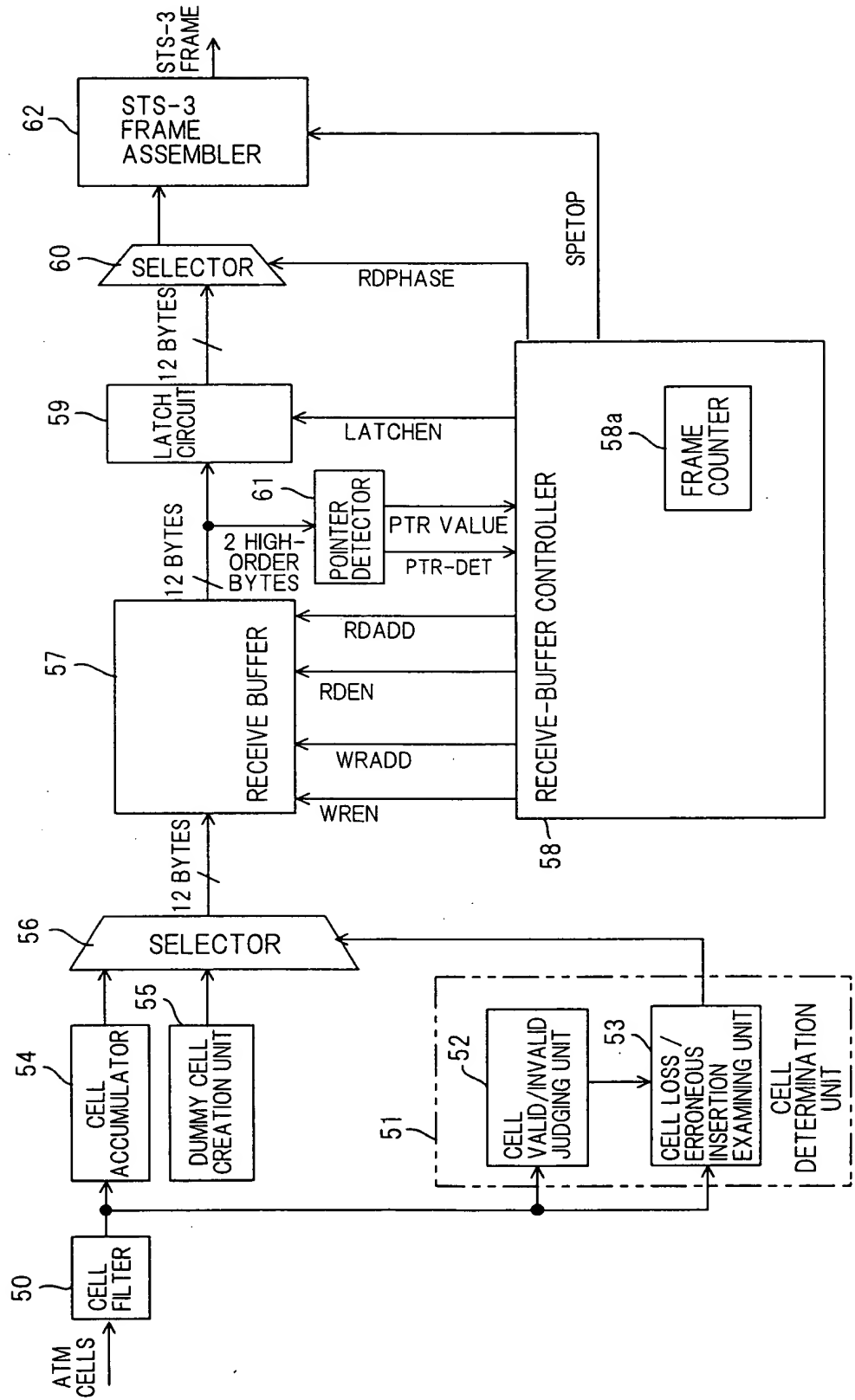


FIG. 21A

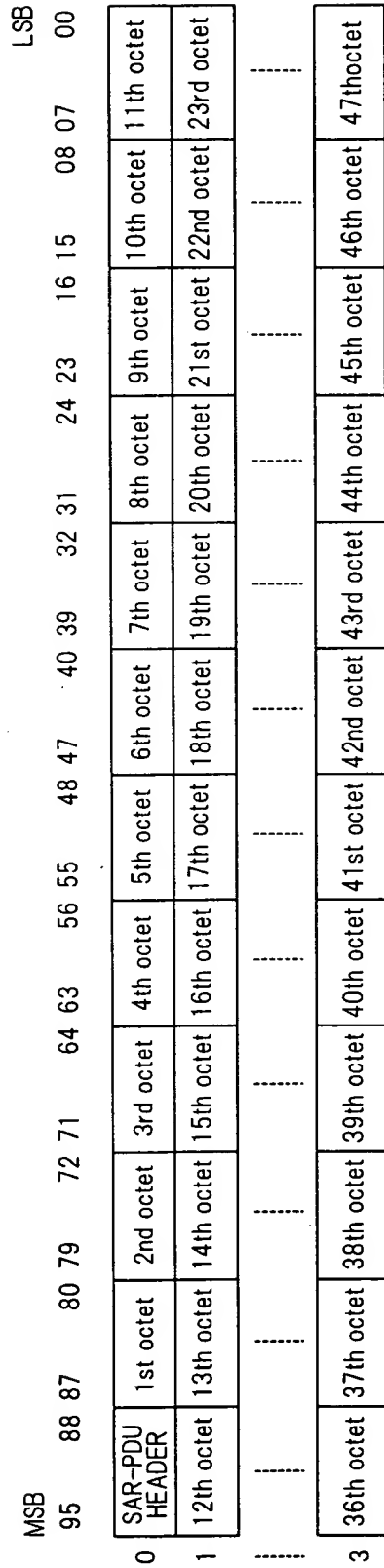


FIG. 21B

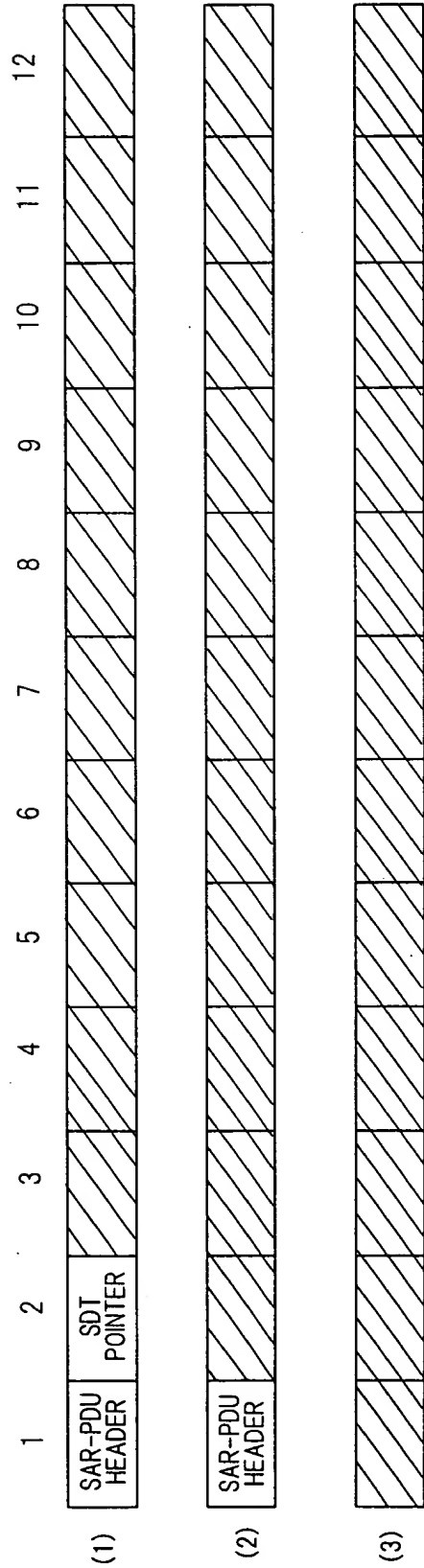


FIG. 22

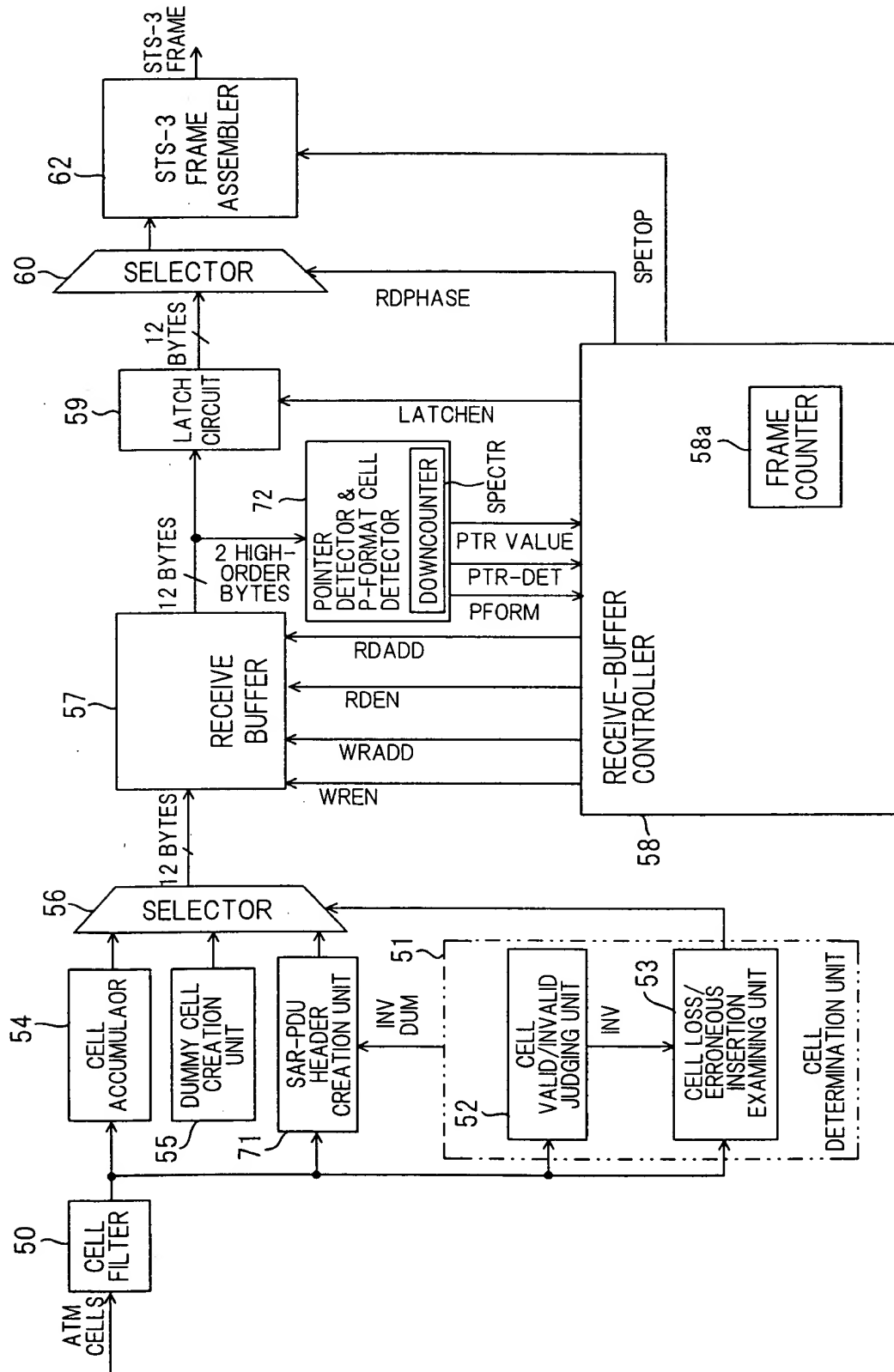


FIG. 23

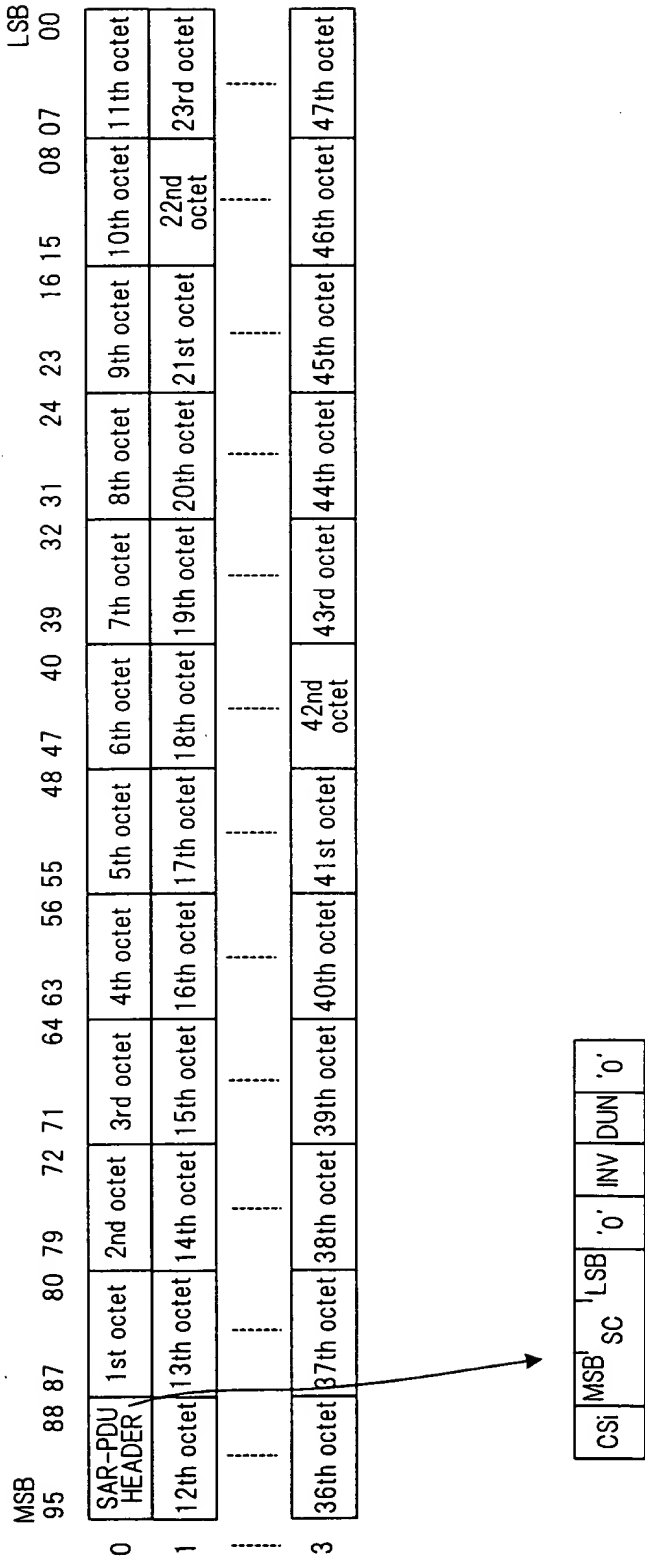


FIG. 24

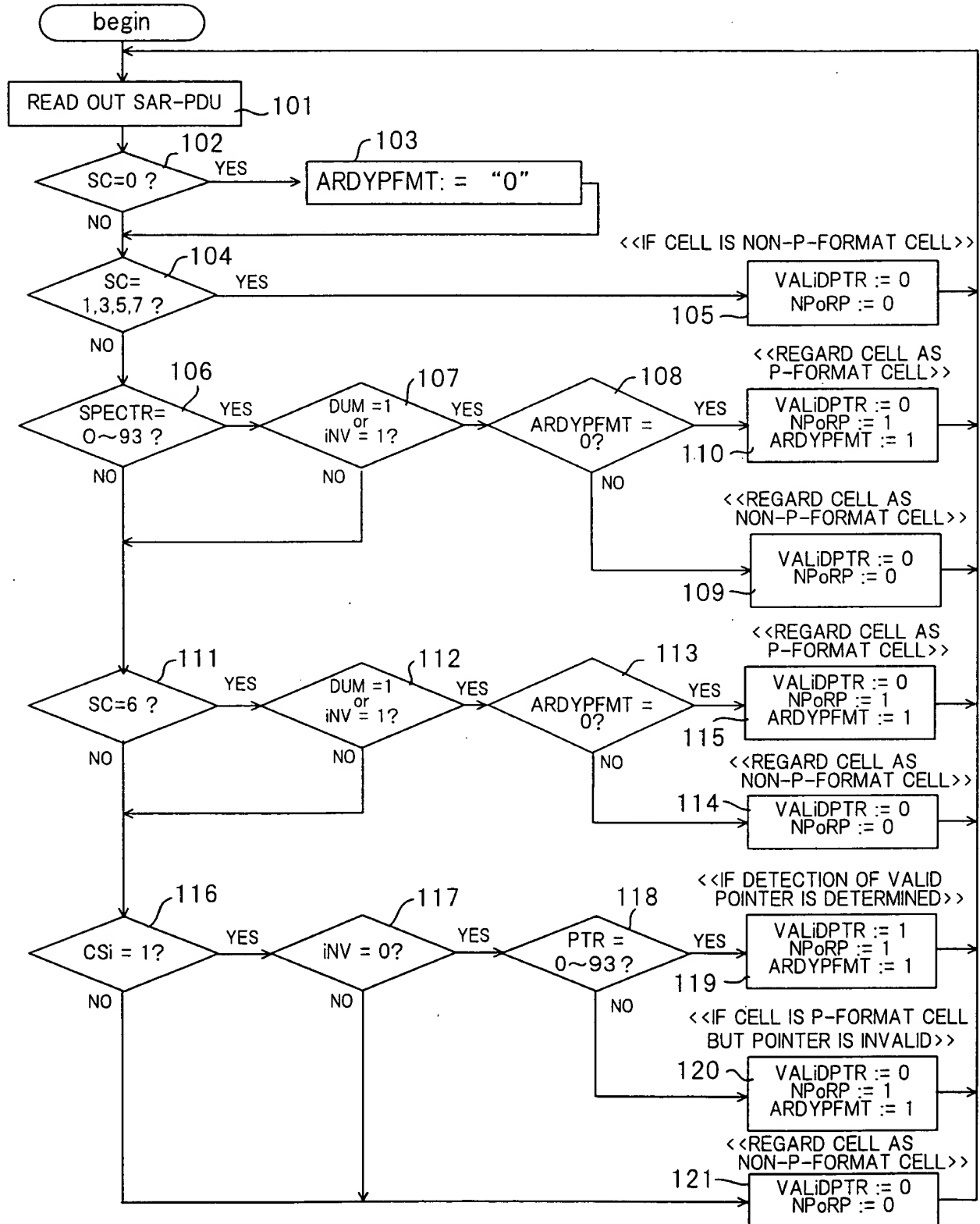


FIG. 25

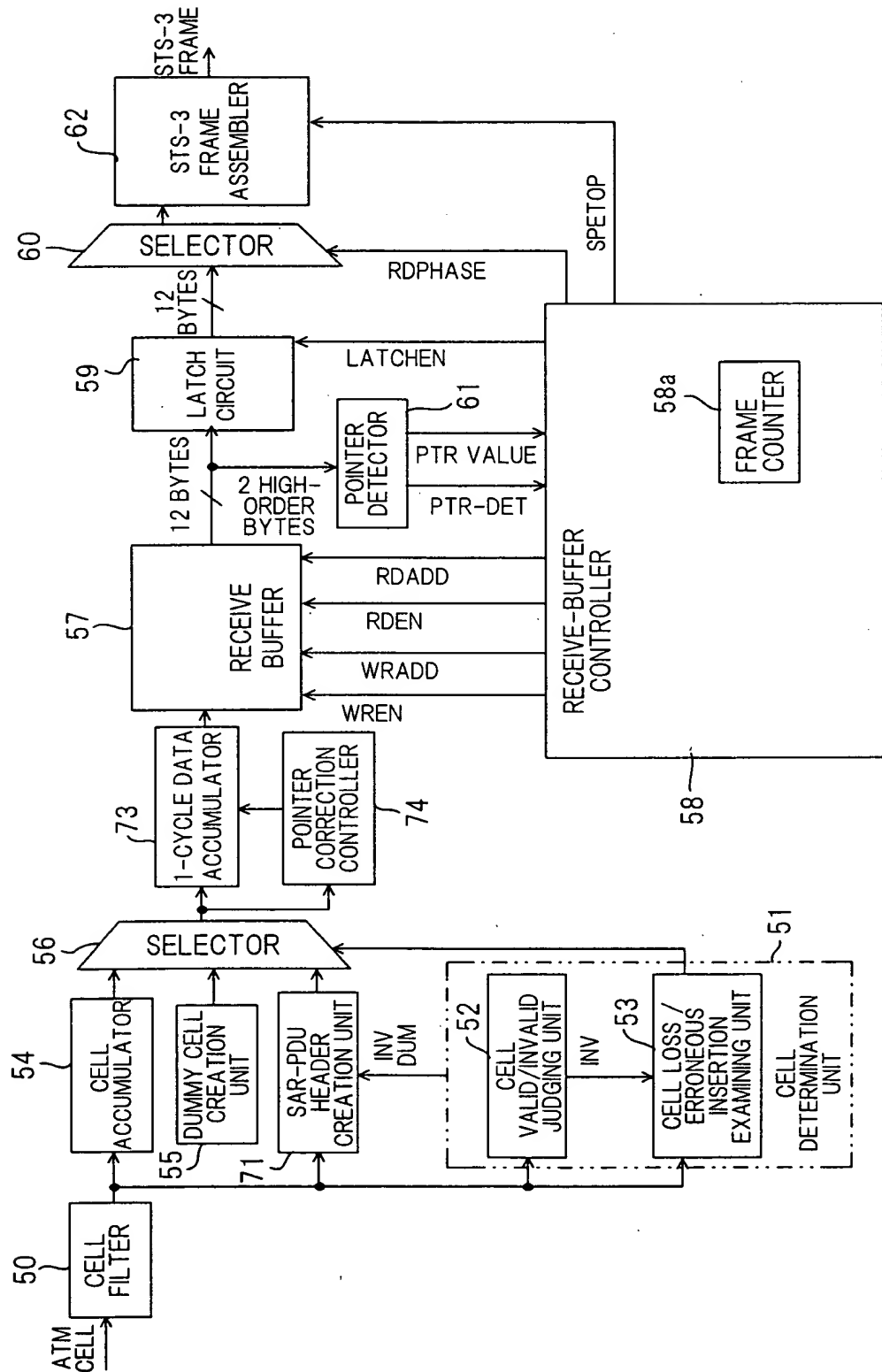


FIG. 26

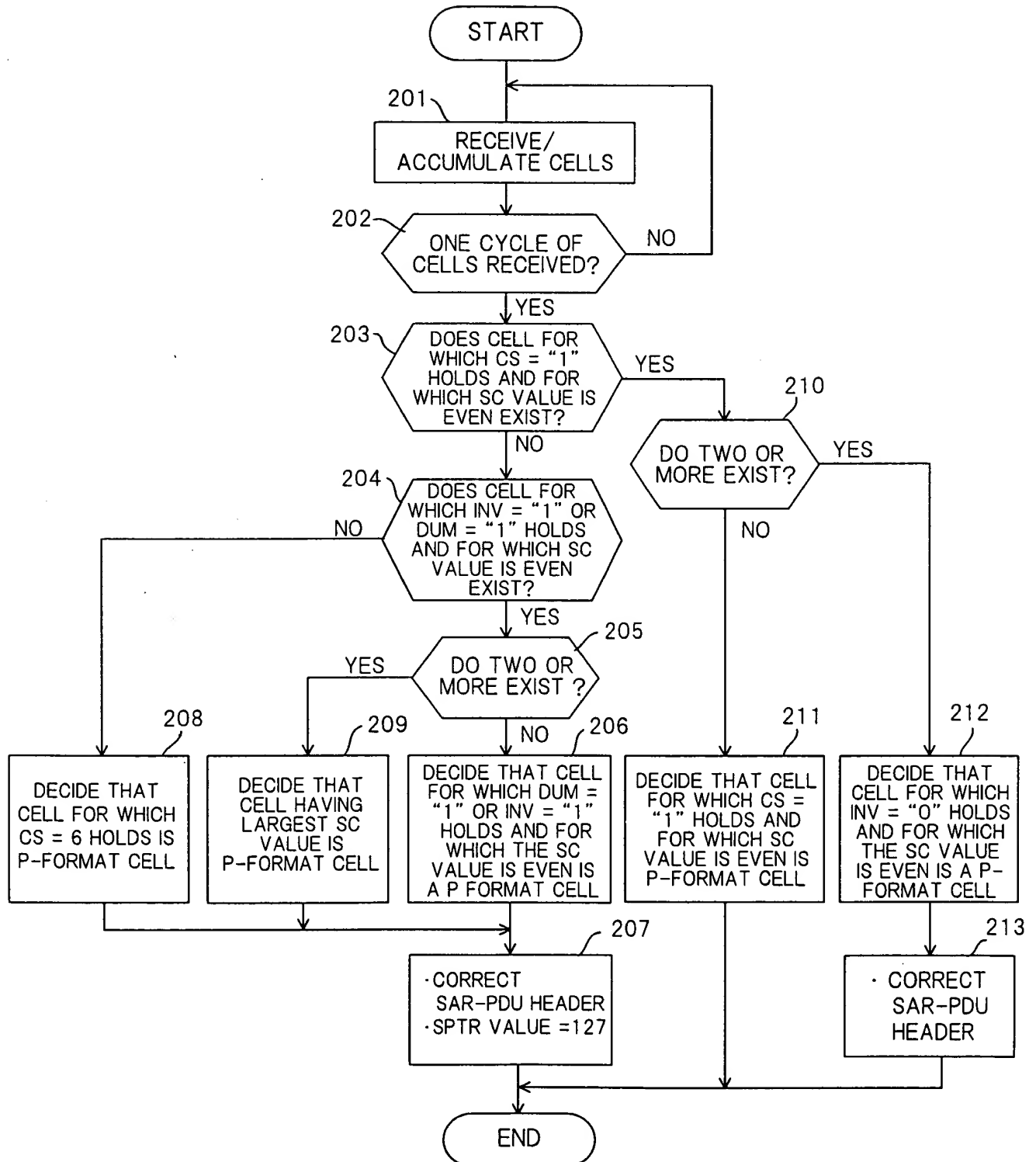


FIG. 27

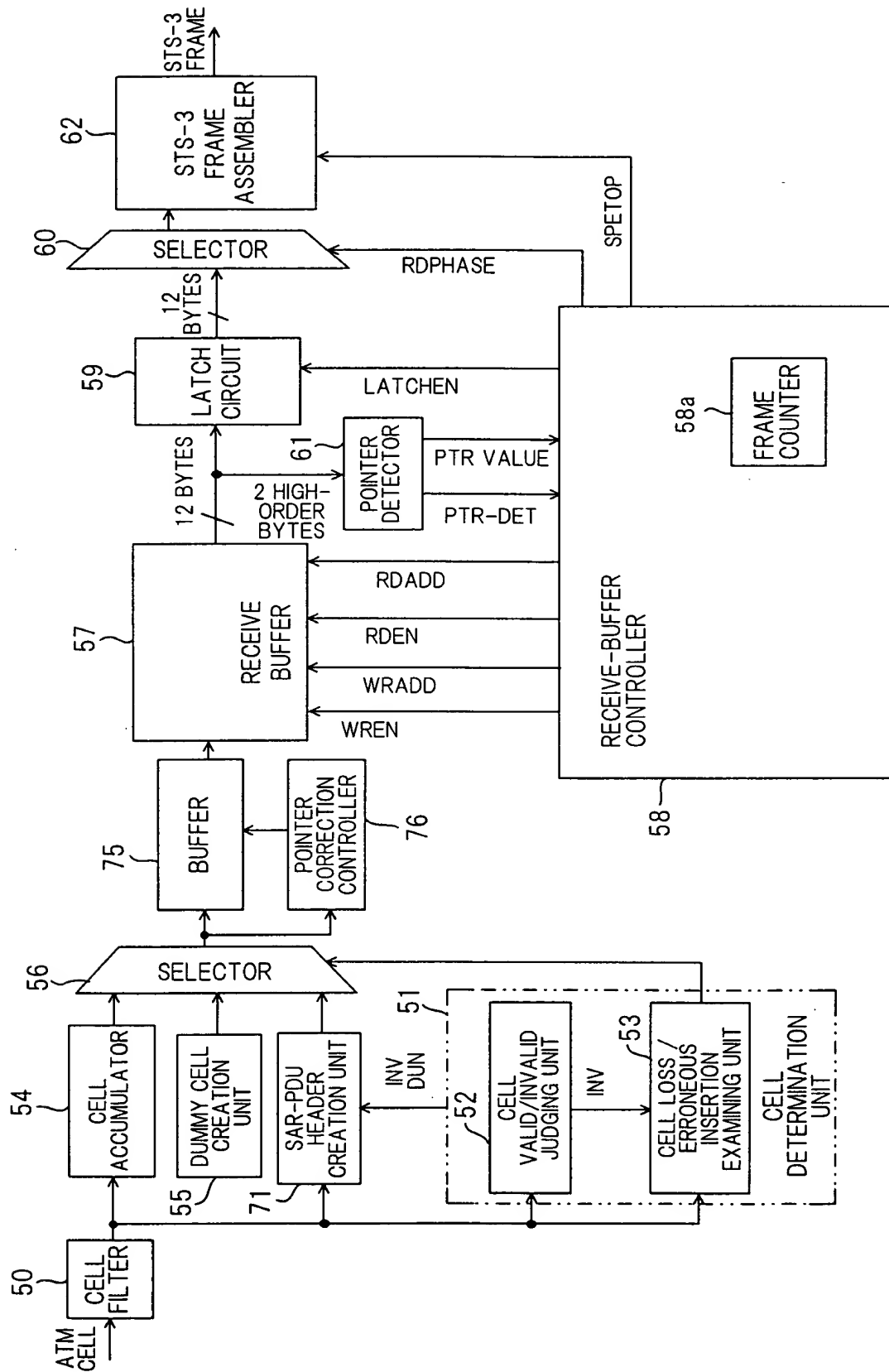


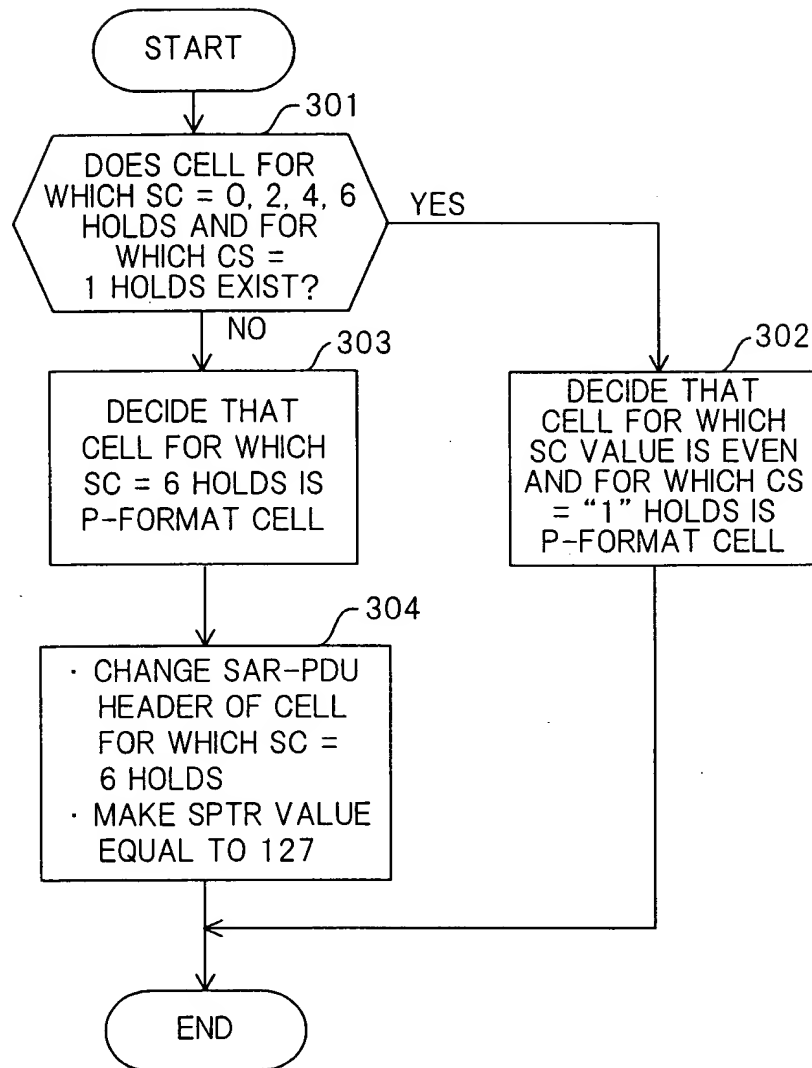
FIG. 28

FIG. 29

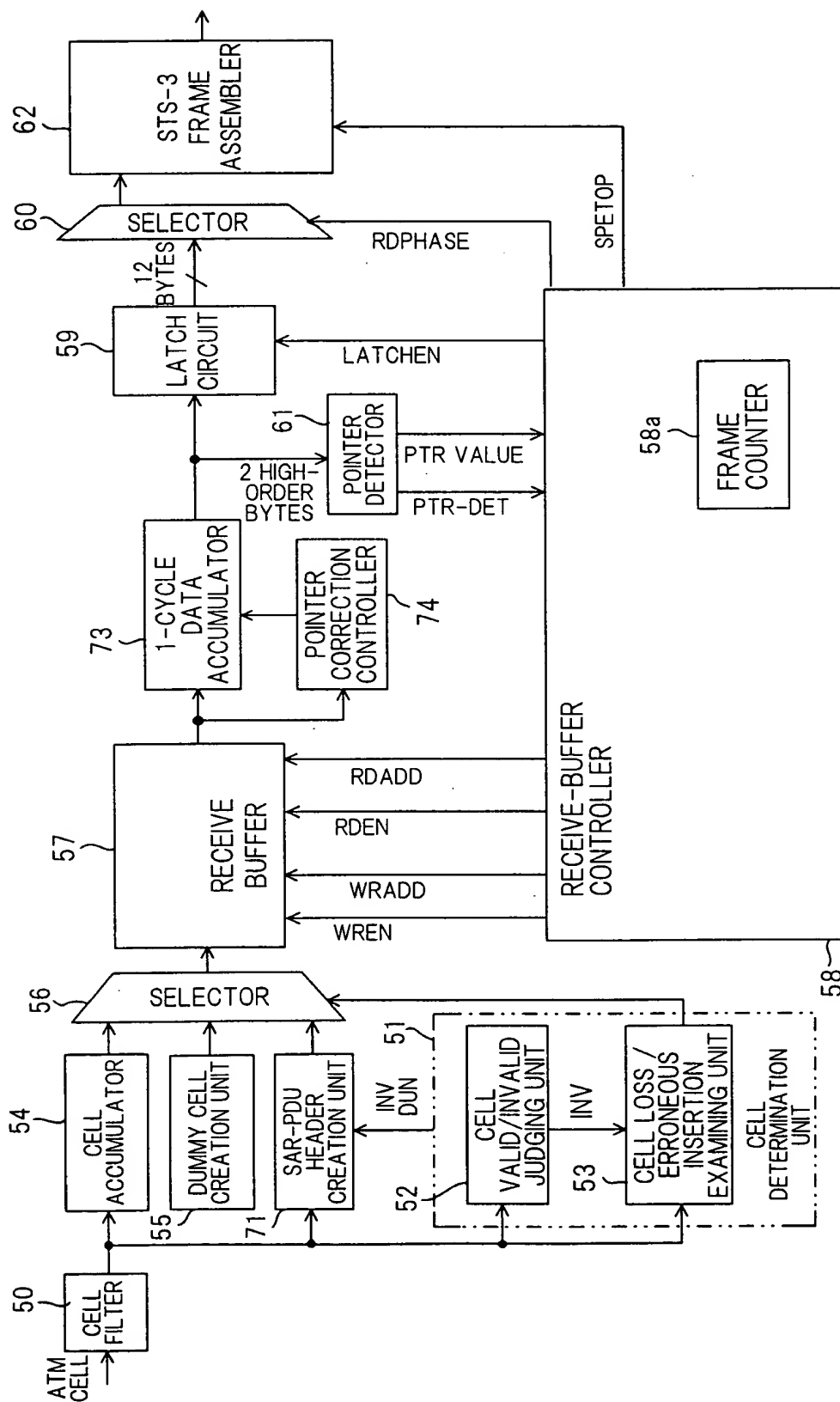


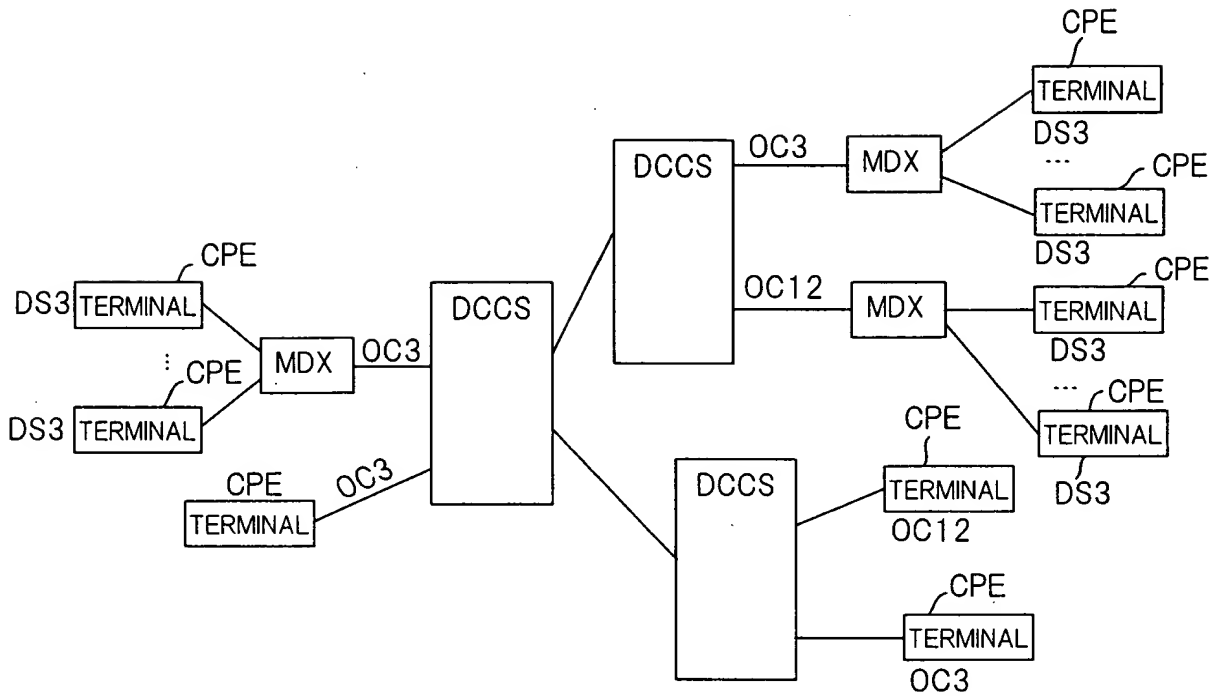
FIG. 30 PRIOR ART

FIG. 31 PRIOR ART